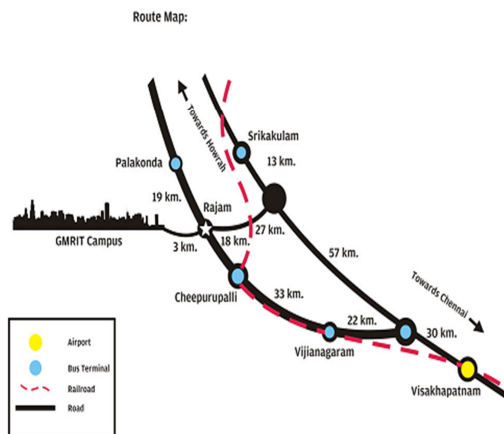


## INSTITUTION:

Established in the year 1997 under the aegis of GMR Varalakshmi Foundation, GMR Institute of Technology is a self-financing Engineering college approved by the All India Council for Technical Education (AICTE) and **Accredited by NBA, NAAC-A grade & an ISO 9001-2008 certified Institution** and an **Autonomous Institute** affiliated to Jawaharlal Nehru Technological University, Kakinada.

Located in Rajam, Srikakulam district of Andhra Pradesh, GMRIT provides its learning community state-of-the-art facilities, infrastructure and a competent faculty. The Institute encourages collaborative learning between industry and academia as a means of reinforcing its curriculum with practical and real world experiences.

## Route Map:



## ORGANISING COMMITTEE

### CHIEF PATRON

Dr. V. Raghunathan  
CEO- GMR Varalakshmi Foundation

### PATRON

Dr. B.S. Nagendra Parashar  
Director-Education, GMRVF

### CHAIRPERSON

Dr. C.L.V.R.S.V. Prasad  
Rector & Principal

### ADVISORY BOARD

Prof. B.I. Neelgar, HOD-ECE  
Dr. Birendra Biswal, Prof. & Assoc. Dean (R & D)  
Dr. S.V. Ramana, Prof. & Assoc. Dean (A)  
Dr. M.V. Nageswara Rao, Prof. & Assoc. Dean (S & F)

### COORDINATOR

Mr. G. Suresh, Asst. Professor

### MEMBERS

Mr. D. Tirumala Rao, Assoc. Prof.  
Mr. P.M.K. Prasad, Assoc. Prof.  
Mr. V. Jagan Naveen, Asst. Prof.  
Mr. G. Manmadha Rao, Assoc. Prof.  
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Mrs. T. Geetamma, Sr. Asst. Prof.  
Mr. D.V. Ramana, Asst. Prof.  
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Mr. K. Krishna Kishore, Asst. Prof.  
Mrs. SSD Kameswari, Asst. Prof.  
Mr. J.V. Suman, Asst. Prof.  
Mr. B. Anil Kumar, Asst. Prof.  
Mr. D. Srinivasa Rao, Asst. Prof.



## Two-Day National Workshop

On

## VLSI Applications to RADAR & Image Processing (VARIP)

28-02-2014 & 01-03-2014



Organized by

Department of ECE

GMR INSTITUTE OF TECHNOLOGY

GMR Nagar, Rajam – 532 127

Srikakulam (Dist.)

Andhra Pradesh, INDIA

Visit us at [www.gmit.org](http://www.gmit.org)

### **OBJECTIVE:**

VLSI has been around for a long time, there is nothing new about it ... but as a side effect of advances in the world of computers, there has been a dramatic proliferation of tools that can be used to design VLSI circuits. As well the present world requires faster processing of digital data such as image and RADAR signals. Obviously, there is very much demand to design VLSI architectures to process RADAR and Image signals at high speeds and at low power consumption. The need of the hour is to introduce the fundamental concepts and to explore the avenue for implementation of these designs.

This workshop focuses on the applications of VLSI to RADAR and Image processing. The goal is to provide a forum for the exchange and discussion of ideas, expertise and experiences on VLSI design. The workshop creates a platform for the young researchers and post-graduate students to have hands-on experience on VLSI Design tools. The workshop will endeavor to prepare models of Instruction Laboratory, R&D Laboratory , Advance Course, with a view to ensure preparedness of the engineering graduates leaving our educational institutes to meet the industry expectations and strengthen the research activity in VLSI and its allied fields.

### **WORKSHOP TOPICS:**

- ✓ Analog & Digital VLSI Design
- ✓ VLSI CAD Tools (Mentor Graphics)
- ✓ FPGA and ASIC Implementation
- ✓ Applications of RADAR & Image Processing
- ✓ VLSI Architectures for RADAR & Image Processing

### **RESOURCE PERSONS**

1. Dr.P.Srihari Rao, Professor, Dept of ECE, NIT Warangal..
2. Dr.Ch.Srinivasa Rao, Professor, Dept of ECE, JNTUK UCEV, Vizianagaram
3. Dr. N.Balaji, Professor, Dept of ECE, JNTUK UCEV, Vizianagaram
4. Mr. K.Bala, Sr.Application Engineer, Trident Tech Labs, Hyderabad.

### **ELIGIBILITY**

Faculty members of Engineering / Polytechnic colleges approved by AICTE / Researchers / Practicing Engineers/Final Year students are eligible.

### **REGISTRATION:**

Faculty / Industry Professionals: Rs.1000/-  
Research scholars: Rs.800/-  
PG Students: Rs.500/-

Participants can register by sending the registration form along with demand draft in favor of Principal, GMRIT payable at Rajam should reach the Coordinator on or before **20<sup>th</sup> February, 2014.**

### **BOARDING & LODGING**

The registration fee includes Registration Kit, boarding & lodging during their stay at GMRIT, RAJAM. Accommodation will be provided subject to availability.

### **MAILING ADDRESS**

**G.Suresh**  
**Coordinator (VARIP),**  
**Department of ECE,**  
**GMR Institute of Technology,**  
**Rajam – 532 127**  
**Srikakulam (Dist), Andhra Pradesh**  
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**GMR Institute of Technology**  
**GMR Nagar, Rajam, Srikakulam(Dist.), A.P,**  
**532127**  
**VLSI Applications to RADAR & Image Processing**  
**(VARIP)**  
**Registration Form**

Name: \_\_\_\_\_

Designation: \_\_\_\_\_

Organization: \_\_\_\_\_

Contact Address: \_\_\_\_\_

I would like to participate as (Please put ✓ mark in the appropriate place)

Academic \_\_\_\_ Industry \_\_\_\_ Students \_\_\_\_

Email: \_\_\_\_\_

Phone Number: \_\_\_\_\_

Registration Fees: \_\_\_\_\_ by DD in favor of Principal, GMRIT, RAJAM

DD No : \_\_\_\_\_ Date: \_\_\_\_\_

Accommodation required: **Yes/No**

Date: \_\_\_\_\_ Signature of Participant \_\_\_\_\_

### **SPONSORSHIP CERTIFICATE**

This is to certify that Mr. / Ms \_\_\_\_\_ is an employee of our institution since \_\_\_\_\_ and is hereby sponsored to attend the workshop on **VLSI Applications to RADAR & Image Processing (VARIP)** at GMRIT, Rajam, Srikakulam during the period 28-02-2014 & 01-03-2014.

Place: \_\_\_\_\_

Date: \_\_\_\_\_ Head of the Institution

Seal