

Institution

Established in the year 1997 under the aegis of GMR Varalakshmi Foundation, GMR Institute of Technology is a self-financing Engineering college approved by the All India Council for Technical Education (AICTE) and accredited by NAAC & an ISO 9001 - 2000 certified Institution, affiliated to Jawaharlal Nehru Technological University, Kakinada. GMRIT awarded as Second Most Clean Campus in India in AICTE Clean Campus Award 2017 on October 2nd 2017. GMRIT is ranked in the band of 151 - 200 at National Level by NIRF, MHRD.

Located in Rajam, Srikakulam district of Andhra Pradesh, GMRIT provides its learning community state-of-the-art facilities, infrastructure and a competent faculty. The Institute encourages collaborative learning between industry and academia as a means of reinforcing its curriculum with practical and real world experiences.

GMRIT offers UG & PG courses in Chemical, Computer Science, Electronics & Communication, Information Technology, Civil, Electrical & Electronics, Mechanical Engineering and Power Engineering with well qualified and experienced faculty and sophisticated laboratories.

SPONSORSHIP CERTIFICATE

This is to certify that Dr./Mr./Ms. _____
_____ is an employee of our institution since _____, and is here by sponsored to attend the Two-day national level workshop on "VLSI and System Design for IoT & AI Applications (VIA-19)" at GMRIT, Rajam, Srikakulam on March, 23-24, 2019.

Place:
Date:

Signature of
Head of the Institution

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CEO- Education & Healthcare Wing,
GMR Varalakshmi Foundation

Patron

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Mr. D. Suresh, Assistant Professor, ECE

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Dr. V. Kannan, Prof.

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Mr. K. Chiranjeevi, Asst. Prof.

Mr. P. Ravi Kumar, Asst. Prof.

A Two-Day National Level Workshop
on

VLSI and System Design for IoT & AI Applications (VIA-19)

Organized By
DEPARTMENT OF ECE

March 23 - 24, 2019



GMR INSTITUTE OF TECHNOLOGY

GMR Nagar, Rajam – 532 127
Srikakulam (Dist.), Andhra Pradesh
Visit us at www.gmr.it.org

In association with



Preamble

The objective of this workshop is to provide a platform to deliberate and discuss various aspects of IoT, Artificial Intelligence applications in VLSI and ASIC design flow. By the end of this workshop the participants should be able to

- Understand the requirements of IoT and AI applications from hardware and IC Design perspective
- Identify the influence of IoT and AI on IC & System Design
- Justify the need for PLD's/FPGA's for IoT based system design
- Explain FPGA and ASIC Design challenges in IoT era
- Explain the new updates from Cadence for system Design and IC Design requirements
- Perform digital IC flow (Synthesis, Floor planning, P & R, Pre and Post layout STA and Physical Verification) using latest version of Cadence EDA tools for the given design

Focus areas of the Workshop

1. VLSI & System Design for IoT & AI applications
2. Hands on sessions on FPGA Design flow for IoT applications
3. Impact of IoT/AI on IC Design – Challenges and Opportunities
4. Physical Design flow using Cadence EDA Tools

Department of ECE

The department of ECE is accredited by National Board of Accreditation under TIER-I. The department offers programs at both Undergraduate (B. Tech) and Postgraduate (M. Tech) levels. It provides students with a solid scientific/technical knowledge to improve their research capabilities.

Resource Persons

1. **Damodara M.S,**
Business Manager, Entuple Technologies
2. **Sharath Kanth**
Sr. Application Engineer, Entuple Technologies
3. **Kedharnath**
Application Engineer, Entuple Technologies

Eligibility

Faculty from academic institutions, Research Scholars, PG Students of Electronics & Communication Engineering discipline and Industry personnel. The number of participants is limited to 30.

Important Dates

Last date for Registration: 16th March 2019
Workshop Date: 23rd -24rd March 2019

Registration

Faculty/Research Scholar : Rs.1000
PG Students : Rs. 750
Participant from Industry : Rs.1500

Application form duly filled along with a D.D. drawn in favor of **The Principal GMRIT**, payable at Rajam (SBI/Andhra Bank) should be sent on or before **20th March, 2019** to the mailing address below.

Mailing Address

The Organising Secretary,
Dept. of Electronics & Communication Engineering
GMR Institute of Technology,
Rajam 532 127, Srikakulam (Dist.), A.P.
Mobile : +91 8895865369 / +91 9494011543
E-Mail : govindarao.l@gmr.it.org

Boarding and Lodging

Free boarding will be provided during the period of the program and free lodging (limited) will be provided at GMRIT Hostels, Rajam, on first come first serve basis.

Last date for registration: 16th March

A TWO DAY NATIONAL LEVEL WORKSHOP ON VLSI and System Design for IoT & AI Applications (VIA-19)

On
March 23-24, 2019

REGISTRATION FORM

Participant's Name:

Qualification:

Department:

Organization:

Address for
Communication:

Mobile Number:

E-mail :

Registration Fee:

DD No.: Dt.:

Name of the Bank:

Accommodation required: Yes/No

Date: Signature of Participant